



SRINIVASA EDUCATIONAL SOCIETY'S
PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)

Approved by AICTE and Govt. of Andhra Pradesh, Accredited by NAAC(A Grade) | Recognized under 2(f) & 12(B) of UGC
 Permanently Affiliated to JNTUK, Kakinada, A.P. | ISO 9001:2015, ISO 14001:2015 and ISO 50001:2018 Certified Institution
 NH-16, Near Valluramma Temple, ONGOLE - 523 272, A.P., India, Ph.: 08592 278315, 9581456310 | www.pace.ac.in

Lr No: PACE(A)/ECS/2024-25/EE/55

28-04-2025.

CIRCULAR

It is hereby informed that the III B.Tech II Semester (R21) Regular (2022 Admitted Batch) and Supplementary (2021 Admitted batch) End Examinations have been rescheduled in view of the upcoming NPTEL examinations, to avoid any overlap and to facilitate student's participation in both.


The revised schedule of these examinations is as follows.

Timing of the Semester End Examination: 10:00 am to 1:00 pm					
Branch	Date & Day				
	06-05-2025 (Tue Day)	08-05-2025 (Thu Day)	10-05-2025 (Sat Day)	13-05-2025 (Tue Day)	15-05-2025 (Thu Day)
CIVIL	Introduction to Database Management Systems	Employability Skills	Design and Drawing of Steel Structures	Water Resources Engineering	Foundation Engineering
	Environmental Impact Assessment				
EEE	Utilisation Of Electrical Energy	Design thinking for Innovation	Power system analysis	Switch gear and protection	Linear & Digital IC Applications
	Power Semiconductor Drives				
ME	Renewable Sources of Energy	Constitution of India	Metrology & Measurements	Heat & Mass Transfer	Design of Machine Elements -II
	Production Planning Control				
ECE	Optical Fiber Communication	Intellectual Property Rights	Microwave & Radar Engineering	Internet of Things	VLSI Design
	Digital Image Processing				
CSE	Introduction to Simulation Software	Entrepreneurship and Innovation	Cryptography and Network Security	Advanced Java and Web Technologies	Automata Theory and Compiler Design
CSIT	Management Science	Design thinking for Innovation	Cryptography and Network Security	Machine Learning	Automata Theory and Compiler Design
IT	Management Science	Design Thinking for innovation	OOAD Using UML	Machine Learning	Design and Analysis of Algorithms
CSE (IOT & CSBT)	Embedded Systems	Design Thinking for innovation	Sensor Network Technologies	Reinforcement Learning	Design and Analysis of Algorithms
	Management Science				
AIDS	Introduction to Simulation Software	Design Thinking For Innovation	Data Science	Deep Learning	Design & Analysis of Algorithms

AIML	Introduction to Simulation Software	Design Thinking For Innovation	Data Science	Deep Learning	Design & Analysis of Algorithms
CSE (RL)	IPR&PATENTS	Entrepreneurship and Innovation	Cryptography and Network Security	Advanced Java and Web Technologies	Automata Theory and Compiler Design

NOTE:

- i) Any Omissions or clashes in this Time Table may please be informed to the Controller of Examinations immediately
- ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual


 CONTROLLER OF EXAMINATIONS
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PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)
VALLUR, ONGOLE-523 272, ANDHRA PRADESH


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Copy to :

- : All HoD's for circulation to the Students
- : Dean Academics - for information
- : Notice board at Exam Cell & System
- : Web design Coordinator
- : Coordinator, IQAC - for information
- : Administrative Officer- for information
- : Office File

EEE - KUNDAI

ASMS - G

AIML - all

CSE - the

ECE - 28/4/25